	Jo	bb Title	Research Associate - RF Design Engineer				
No. of Positions			01				
	School/In	stitute/Centre	SINES				
1	Purpose of Position						
	The RF Design Engineer shall be responsible to design the RF front-end of the radar system.						
2	Functions/Duties (Key Responsibilities)						
	RF Design Engineer should be able to: • Design, Analyze and work with RF modules & sub-systems • Design PCBs • Stuff and test the designed modules • Handle RF equipment • Knowledge of precautions for handling RF components and equipment						
3	Requirements						
Qualifications		Essential Bachelors in Electronics/Electronics or relevance fro with experience fro recognized institu	rical Master's degree in EE or ant field relevant field with experience in RF & Microwave				
E	xperience	1 year experience in Microwave					
 RF & Microwa Report writing PCB designing Circuit designi Knowledge of Able to use test Able to commit 		Able to use testAble to commun	skills kills g skills F/EMC compliance standards equipment/instruments nicate with other team members				
4	How to Apply						
Please email your resume to rrl@sines.nust.edu.pk only if you fulfil the criteria mentioned above with job title "RF Design Engineer" in the subject field by 07th January 2025.							

	Job Title	Task Based Resource - RSP		
	No. of Positions	01		
	School/Institute/Centre	SINES		
1	Purpose of Position			

We are seeking a highly motivated Radar Signal Processing Engineer to join our team. The candidate will play a critical role in the development, implementation, and optimization of radar signal processing algorithms. This role involves working on signal detection, filtering, beamforming, target identification & tracking, and Doppler analysis.

2 Functions/Duties (Key Responsibilities)

Algorithm Development:

Design, implement, and test radar signal processing algorithms, including detection, filtering, FFTs, beamforming, and tracking. Develop solutions for signal denoising, interference mitigation, and clutter suppression. Optimize signal processing algorithms for deployment on FPGAs (using HDL/Verilog/VHDL), DSPs, or embedded systems. Integrate radar signal processing solutions into real-time systems.

- Radar Data Analysis:
 - Analyze radar signal data (IQ data, time-domain, and frequency-domain) for performance evaluation and system optimization. Perform Doppler processing and range estimation for target identification.
- Simulation and Modeling:
 - Develop radar signal processing models using tools like MATLAB or SystemVue. Simulate radar performance in different environments and refine algorithms accordingly.
- System Testing and Validation:
 - Support laboratory and field testing for radar systems. Analyze test results and troubleshoot performance issues.
- Collaboration:

Work closely with cross-functional teams including hardware engineers, software developers, and system architects. Contribute to technical documentation, including algorithm design documents and performance reports.

		Essential	Desired
Qualifications Experience Knowledge/ Skills/Abilities/ Competencies		Bachelors in Electronics/Electrical Engineering or relevant field with experience from HEC recognized institution	Master's degree in EE or relevant field with experience in Radar Signal Processing
		Min 1 year in similar role	2 years experience in Radar Signal Processing
		 Proficiency in signal processing techniques (FFT, FIR/IIR filtering, beamforming, Doppler processing). Hands-on experience with tools like MATLAB/Simulink, Python, or C/C++. Knowledge of Radar signal models, waveforms, and clutter analysis. Experience in implementing algorithms on FPGAs, DSPs, or embedded platforms is a plus. Able to use test equipment/instruments. Understanding of radar concepts such as range, Doppler effect, SNR, and clutter rejection. Familiarity with Pulse-Doppler Radar, MIMO Radar, and Phased Arrays. Able to communicate with other team members 	
4		How to A	pply

Please email your resume to rrl@sines.nust.edu.pk only if you fulfil the criteria mentioned above with job title "Task based Resource - RSP" in the subject field by 07th January 2025.